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Docket No.: SON-3058  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Masaki Murase et al.

Application No.: 10/564,473

Confirmation No.: 9263

Filed: January 13, 2006

Art Unit: 2629

For: DELAY TIME CORRECTION CIRCUIT,  
VIDEO DATA PROCESSING CIRCUIT, AND  
FLAT DISPLAY DEVICE

Examiner: G. Sitta

**STATUS INQUIRY**

BOPAI  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

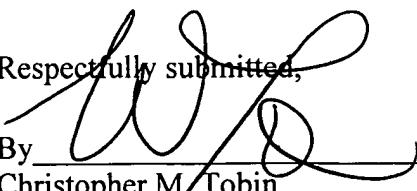
Dear Sir:

It is respectfully requested that the representative named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive further communication from the Board of Patent Appeals and Interferences.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 18-0013, under Order No. SON-3058 from which the undersigned is authorized to draw.

Dated: September 21, 2010

Respectfully submitted,

By   
Christopher M. Tobin

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